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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	ATTORNEY DOCKET NO. CONFIRMATION NO.	
10/825,481	04/14/2004	Vaishnav Srinivas	030333	030333 8117	
23696	7590 09/18/2006		EXAMINER		
QUALCOMM INCORPORATED 5775 MOREHOUSE DR. SAN DIEGO, CA 92121			TON, MY TRANG		
			ART UNIT	PAPER NUMBER	
ic-	,		2816		
			DATE MAILED: 09/18/2006		

Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)		
10/825,481	SRINIVAS ET AL.		
Examiner	Art Unit		
My-Trang N. Ton	2816		

	My-Trang N. Ton	2816					
The MAILING DATE of this communication appe	ars on the cover sheet with the c	orrespondence add	ress				
THE REPLY FILED 31 August 2006 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.							
1. The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:							
a) The period for reply expires 3 months from the mailing date b) The period for reply expires on: (1) the mailing date of this A		in the final rejection wh	iahawaria latas II.				
no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection. Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN							
TWO MONTHS OF THE FINAL REJECTION. See MPEP 7. Extensions of time may be obtained under 37 CFR 1 136(a). The date	ub.u7(t). on which the netition under 37 CFP 1.1	36(a) and the appropria	te ovtension foo				
Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL							
 The Notice of Appeal was filed on A brief in comp filing the Notice of Appeal (37 CFR 41.37(a)), or any exter a Notice of Appeal has been filed, any reply must be filed 	nsion thereof (37 CFR 41.37(e)), to	avoid dismissal of th	is of the date of e appeal. Since				
AMENDMENTS	walling the time period set forth in e	77 Of IX 41.57 (a).					
3. The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will <u>not</u> be entered because (a) They raise new issues that would require further consideration and/or search (see NOTE below); (b) They raise the issue of new matter (see NOTE below);							
(c) They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or							
(d) They present additional claims without canceling a		ected claims.					
NOTE: <u>See Continuation Sheet</u> . (See 37 CFR 1.116 and 41.33(a)).							
4. The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324). 5. Applicant's reply has overcome the following rejection(s):							
 5. Applicant's reply has overcome the following rejection(s): 6. Newly proposed or amended claim(s) would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s). 							
	⊠ will not be entered, or b) □ wil	ll be entered and an e	explanation of				
7. For purposes of appeal, the proposed amendment(s): a) will not be entered, or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended. The status of the claim(s) is (or will be) as follows:							
Claim(s) allowed:							
Claim(s) objected to: Claim(s) rejected: <u>1-8, 10-12, 14-23, 25-27</u> . Claim(s) withdrawn from consideration:							
AFFIDAVIT OR OTHER EVIDENCE							
 The affidavit or other evidence filed after a final action, bu because applicant failed to provide a showing of good and was not earlier presented. See 37 CFR 1.116(e). 	t before or on the date of filing a No d sufficient reasons why the affiday	otice of Appeal will <u>no</u> it or other evidence is	t be entered necessary and				
9. The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will <u>not</u> be entered because the affidavit or other evidence failed to overcome <u>all</u> rejections under appeal and/or appellant fails to provide a showing a good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).							
10. ☐ The affidavit or other evidence is entered. An explanation REQUEST FOR RECONSIDERATION/OTHER	n of the status of the claims after e	ntry is below or attach	ed.				
11. The request for reconsideration has been considered bu	t does NOT place the application in	n condition for allowar	ce because:				
12. Note the attached Information Disclosure Statement(s). (PTO/SB/08) Paper No(s) 13. Other:							
		my mangle)				
		My-Trang N. Ton Primary Examiner					

Art Unit: 2816

Continuation of 3. NOTE: The limitation "the first NFET is larger than the second PFET creating unequal delays .. the second NFET is larger than the first PFET, creating unequal delays ... the output of the first inverter rising to logic one" (claim 1); "each of the first and second outputs of the two cross-coupled inverters falls to logic zero before the other of the first and second outputs rises to logic one ..." (claim 10); " the first output has a first delay charging relative to the second output discharging ... by the unequal transistor sizes of the cross-coupled inverters" (claim 23); " first time constant to charge a first output of the cross-coupled inverters is greater than ... a fourth time constant to discharge the first output so that the first output discharges before the second output charges" (claim 26) required further searches and/or consideration;